

## 1.4 Specifications

Below is a table outlining the specifications of the SPARC CPU-5CE board.

**Table 1: Specifications of the SPARC CPU-5CE**

Processor	microSPARC-II
Clock Frequency	85 MHz
SPECint92	64.0
SPECfp92	54.6
MIPS	112.5
MFLOPS	14.9
Memory Management Unit	SPARC Reference MMU
Data/Instruction Cache	8 Kbyte/16 Kbyte
Shared Main Memory	8, 16, 32 or 64 Mbyte DRAM
SBus Slots	2, mechanically compatible to CPU-2CE and CPU-3CE
SCSI with DMA to SBus	NCR89C100 10 Mbytes/sec 53C90A superset I/O on P2
Ethernet with DMA to SBus	NCR89C100 10 Mbits/sec AM7990 compatible I/O on front panel or P2
Parallel port with DMA to SBus	NCR89C100 3.4 Mbytes/sec Centronics compatible Uni- or bidirectional I/O on P2 via switch matrix
Floppy Disk Interface	NCR89C105 250, 300, 500 Kbytes/sec and 1 Mbytes/sec 82077AA-1 compatible I/O on P2 via switch matrix
Serial I/O	NCR89C105 Two ports with RS-232 configuration, Optional RS-422/485 via hybrid modules 8530 compatible I/O on front panel and P2
Keyboard/Mouse Port	Sun compatible, on front panel
Counters/Timers	Two 22-bit, programmable

**Table 1: Specifications of the SPARC CPU-5CE (Continued)**

Boot Flash Memory	512 Kbyte (1 Mbyte Option) On-board programmable Hardware write protection
User Flash Memory	2 Mbyte (optional) On-board programmable Hardware write protection
RTC/NVRAM/Battery Usable Memory	MK48T08 6 Kbyte
VMEbus Interface	32-bit master/slave, IEEE-1014
Additional Features	Reset and Abort switches Status LEDs on the front panel HEX display on the front panel Watchdog timer
Firmware	OpenBoot with diagnostics
Power consumption	+5V      5.0 A (No SBus Module installed) +12V     0.7A -12V     0.2A
Environmental Conditions Temperature (Operating) Temperature (Storage)  Humidity	0° C to +50° C -40° C to +85° C -20° C to +70° C (with installed RTC) 0% to 95% noncondensing
Board Size	Single Slot 6U VMEbus 160.00 x 233.35 mm 6.29 x 9.18 inches

## 1.5 Product Nomenclature

FORCE COMPUTERS' SPARC CPU-5CE is available in several memory and speed options. Consult your local sales representative to confirm availability of specific combinations.

The table below explains the product nomenclature.

**Table 2: Product Nomenclature**

<b>CPU-5CE/xx-yy-z</b>		
Main Memory (xx)	microSPARC-II Clock Frequency (yy)	User EPROM (z)
8 = 8 Mbyte 16 = 16 Mbyte 32 = 32 Mbyte 64 = 64 Mbyte	85 = 85MHz	0 = 0 Mbyte 1 = 1 Mbyte 2 = 2 Mbyte
<b>Sample Product Nomenclature</b>		
CPU-5CE/16-85-0	85 MHz microSPARC-II CPU board with 16-Mbyte DRAM, 0-Mbyte user flash EPROM, SCSI, Ethernet, floppy disk, parallel and 2 serial I/O ports, 32-bit VMEbus interface, 2 SBUS slots, OpenBoot firmware. User documentation included.	
<b>Other Available Configurations</b>		
CPU-5CE/16-85-2	16 Mbyte, (as above, except 2 Mbyte user flash EPROM)	
CPU-5CE/32-85-2	32 Mbyte, (as above, except 32 Mbyte DRAM)	
CPU-5CE/64-85-2	64 Mbyte, (as above, except 64 Mbyte DRAM)	

### 1.5.1 Ordering Information

This next page contains a list of the product names and their descriptions.